

UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO. : 6,674,509 B2
APPLICATION NO. : 09/965801
DATED : January 6, 2004
INVENTOR(S) : Toru Suzuki

Page 1 of 2

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

COLUMN 1:

Line 60, "generate" should read -- generated --.

COLUMN 4:

Line 11, "According" should read -- According to --.

Line 47, "interface" should read -- interface, --.

COLUMN 6:

Line 39, "i-lines" should read -- i-lines) --.

COLUMN 8:

Line 39, "magnification a" should read -- magnification α --.

Line 46, "and t e" should read -- and the --.

COLUMN 9:

Line 10, "value c" should read --value ϵ --.

Line 39, "perpendicular of" should read -- perpendicular to --.

Line 50, "exposure" should read -- exposure method --.

Line 67, "a to" should read -- a variance to --.

COLUMN 10:

Line 18, "shots," should read -- shots --.

COLUMN 11:

Line 67, "chip J" should read -- chip --.

COLUMN 13:

Line 25, "exposure p" should read -- exposure --.

Line 39, "operation of the manufacturing line in this system, the" should be deleted.

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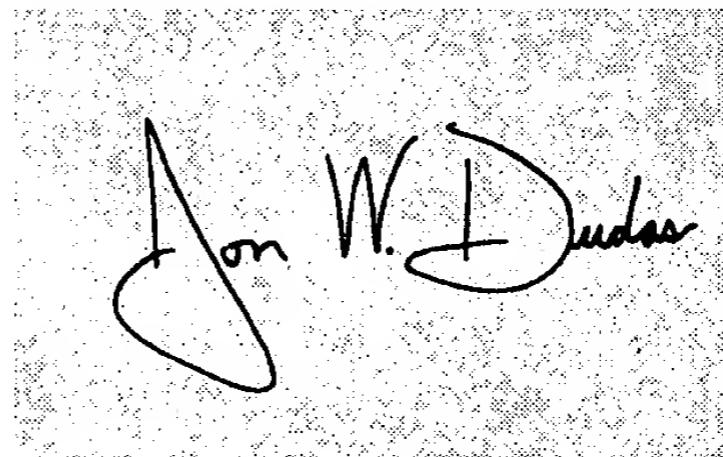
It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

COLUMN 14:

Line 56, "shorted," should read -- shortened, --.

Signed and Sealed this

Twentieth Day of November, 2007



JON W. DUDAS
Director of the United States Patent and Trademark Office